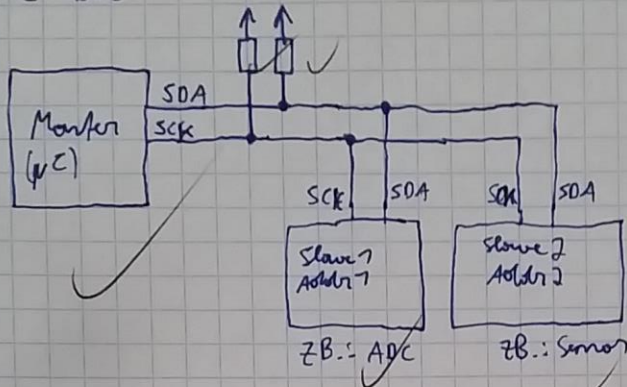


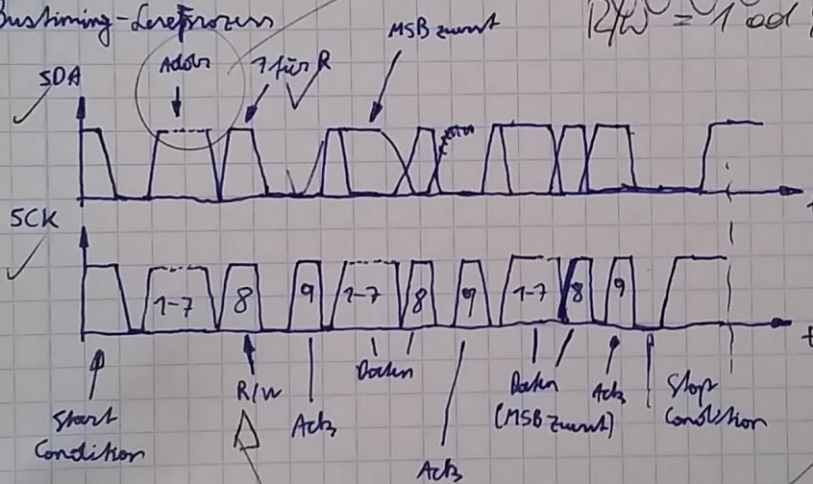
7.03.2015

Rufus Graumann

1) I2C - BSB



Bus timing - Leseprozess



Wurde Rasterfolge?? MSB zuerst
 $R/W = 1 \text{ oder } 0$

(47p)